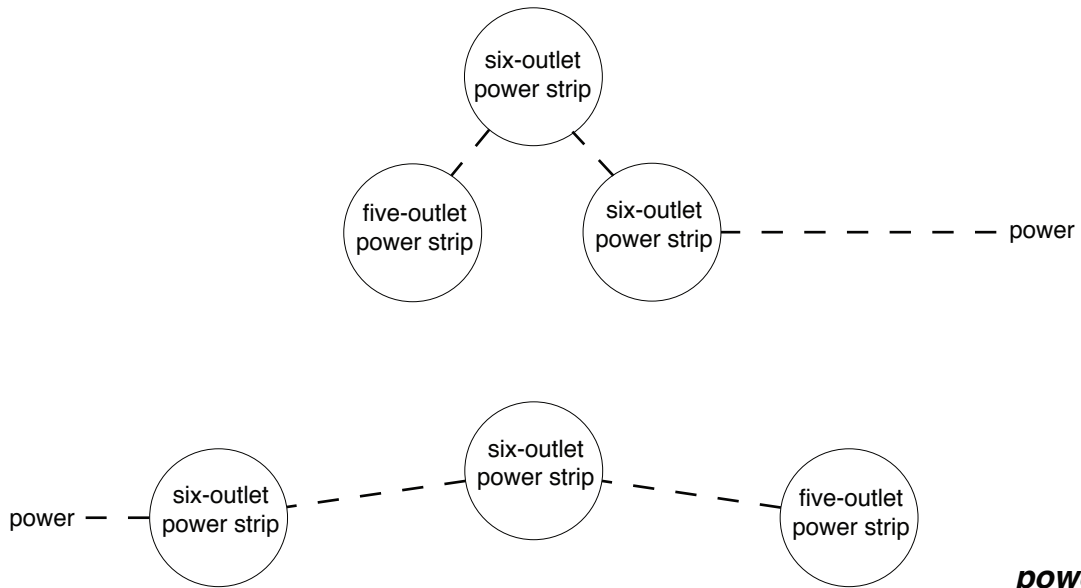
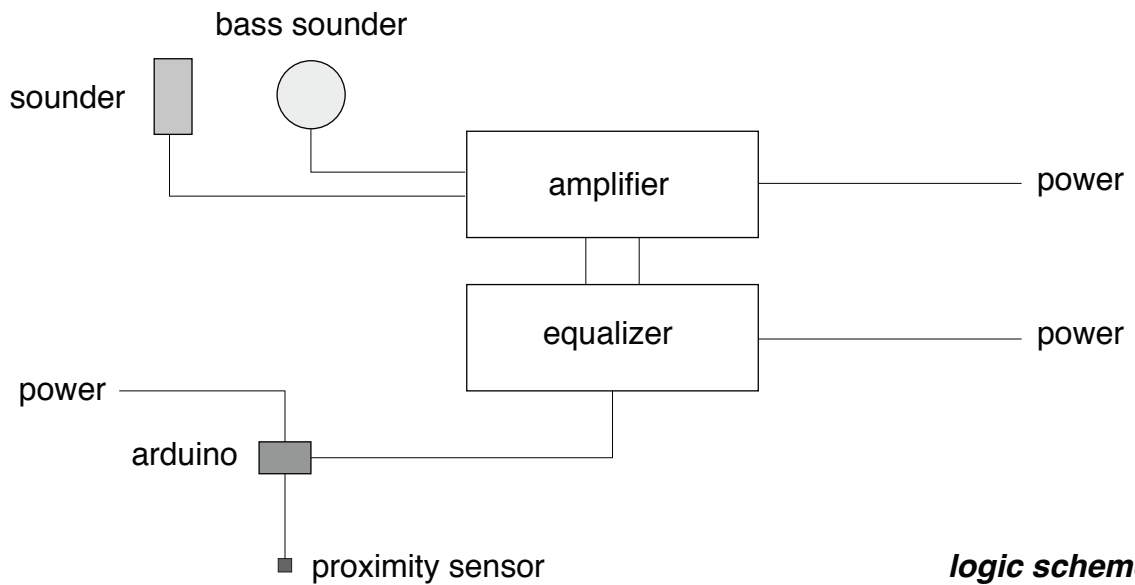


**plan scheme**



**power scheme**



**logic scheme**